NOTICE OF EXPRESS MAILING

January 16, 2007

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Boon et al.

Serial No.: 10/717,421

Filed: November 19, 2003

For: CASTELLATED CHIP-SCALE PACKAGES AND METHODS FOR

FABRICATING THE SAME

Confirmation No.: 7753

Examiner: J. Mitchell

Group Art Unit: 2813

Attorney Docket No.: 2269-5660US

Jocket No.: 2269.
(02-1391.00/US)

Notice of Allowance Mailed:

Approx Enter October 18, 2006

OK to Enter October 18, 2006

AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Amendments to the specification begin on page 3 of this paper;